RAAK

COLLEGE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)

03/08/2020

From

Ms. V. Uvaranjani

Assistant professor/ ECE

RAAK College of Engineering and Technology

Puducherry -110

To

The Principal

RAAK College of Engineering and Technology

Puducherry -110

Respected Sir,

Sub: Requisition for Approval to Conduct Skill Development program / Value added Course on **"20ECE01- High speed system design"**-reg.

This is to bring to your kind notice that the Skill Development Team is planning to conduct a Program on **"20ECE01- High speed system design"** for all the Final Year Electronics and communication Engineering students from 09-08-2020 to 13-08-2020.

The main focus of this program is to provide a better exposure to our students on the High speed system design

The syllabus and course plan structured are not listed in the Pondicherry University Curriculum and the same have been verified and approved by the Principal/HoD/Professors and Skill development team.

Hence, I kindly request you to approve event planned. The details and the necessary proofs are attached with this letter.

Thanking you,

Yours faithfully,

Ms. V. Uvaranjani

AP/ECE

Dr. S. SEENUVASAMURTHI, M.E., Ph.D.
PRINCIPAL
RAAK College of Engineering & Technology
No.1, Muthupillai Palayam Road,
Sulthanpet Post,
Puducherry - 605 110

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COLLEGE OF ENGINEERING AND TECHNOLOGY

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RAAKCET/PRINCIPAL/CIR/AUG2020 **04/08/2020**

CIRCULAR

This is to inform that the Skill Devlopment Team is planning to conduct a value added course on **"20ECE01- High speed system design"** for all the Final Year ECE Department students from 09-08-2020 to 13-08-2020.Students are asked to utilize this opportunity and improve their skills.

PRINCIPAL

Circulation to:

1. All Students
2. All Faculty & Staff Members
3. All HoDs

Copy to:

1. All HODs
2. Office

Dr. S. SEENUVASAMURTHI, M.E., Ph.D.
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
PRESENTS
VALUE ADDED COURSE ON
HIGH SPEED SYSTEM DESIGN

2020-2021

DATE: 09/08/2020 to 13/08/2020

VENUE: RAAKCET

TIME: 09 am to 04 pm

Resource Person:

Mr. R. Rajanthiran
Assistant Professor,
Christ college of Engineering.

For Registration Contact:

Ms. V. Uvaranjani , AP/ ECE.,
8763765484.

HOD

Mr. Ayyapasamy

RAAK College of Engineering & Technology

PRINCIPAL

Dr. S. Seenuvasamurthi

Dr. S. SEENUVASAMURTHI, M.E., Ph.D.
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VALUE ADDED COURSES

2020-2021

Department of Electronics and communication Engineering

20ECE01- High speed system design

Syllabus

Duration: 30 hours

Course Objective:

- To comprehend the fundamental concepts of graphics and animation.
- To gain and understand the acquired knowledge pertaining to 2D and 3D concepts ingraphics.
- To understand the basic 3D modeling and rendering techniques.

Course Outcome:

Upon successful completion of the course students able to

- To understand the concepts of High speed system design primitives and various graphics algorithms.
- Design and demonstrate the 2D object transformation and viewing through graphicsprinciples.
- To understand the various color models and comprehend the complexities of illumination in virtual scenes.
- Have the ability to model the hidden surface and render the respective 3D objects so as to project it on to the screen.

Module 1: Fundamentals of High speed system design **(9 Hours)**

Attributes of Graphics Primitives, Implementation Algorithms for Graphics primitives and attributes-Line drawing: DDA, Bresenham's, Circle generation, Ellipse generation, Implementation style for fill styles: Scan line polygon filling algorithm, Boundary fill and Flood fill, Implementation methods for Antialiasing.

Module 2: 2D Transformation and Viewing **(9 Hours)**

2D transformation: Translation, Scaling, Rotation, Composite transformation, Reflection, Shearing, Raster Transformation - 2D Viewing: Pipeline, Normalization and viewport transformation, 2D Clipping Algorithms: Point, Line, Polygon, Curve, Text

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Module 3: 3D Transformation and Viewing **(9 Hours)**

3D Transformation: Translation, Scaling, Rotation, Reflection, Shearing. 3D Viewing: Projection, Three-Dimensional Viewing concepts, 3D Viewing pipe line, Three-Dimensional viewing coordinate parameters,

Projection transformation: Parallel projection, Orthogonal projection: oblique, Perspective projection, View volume.

Module 4: Color Models and Illumination **(9 Hours)**

Color Models: Chromaticity Diagram, RGB model, YIQ model, CMY model, CMYK model, HSV model, HLS model, Transformation between color models. Illumination models: Lighting Models, Basic Illumination models: Ambient Light, Diffusion Light, Specular reflection.

Module 5: Visible Surface Detection and Surface Rendering **(9 Hours)**

Visible Surface Detection Methods: Back face detection, Depth buffer method, A-Buffer method, Scan-line method, Depth-sorting method, BSP-Tree method, Area-subdivision method, Octree method, Ray-casting method, Curve and Line frame detection, Polygon rendering method.

Course Designed By **Approved By** **Principal**

RAAK College of Engineering & Technology

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CO - ATTAINMENT MAPPING

Sl. No	Register Number	Student Name	CO1	CO2	CO3	CO4
1	17TC2204	ANITHA R	✓	✓	✓	✓
2	17TC2205	DEEPA M	✓	✓	✓	✓
3	17TC2206	DEEPIKA M	✓	✓	✓	✓
4	17TC2207	DEIVAYANAI V	✓	✓	✓	✓
5	17TC2208	IYYAPPAN K	✓	✓	✓	✓
6	17TC2209	KALAIYARASI G	✓	✓	✓	✓
7	17TC2210	KEERTHANA K	✓	✓	✓	✓
8	17TC2211	KEERTHANA K	✓	✓	✓	✓
9	17TC2213	NILAVARASI R	✓	✓	✓	✓
10	17TC2212	MUTHULAKSHMI N	✓	✓	✓	✓
11	17TC2215	NITHIYAKUMARI J	✓	✓	✓	✓

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VALUE ADDED COURSES 2020-2021

Department of Electronics and communication Engineering

20ECE01- High speed system design

COURSE PLAN

S.no	Date	Hours	Time	Topic	Faculty details
DAY -1					
1	09.08.20	1.2	9AM -11AM	Attributes of Graphics Primitives, Implementation Algorithms for Graphics primitives and attributes-Line drawing: DDA, Bresenham's, Circle generation	Mr. S. Parthiban, & Mrs.S.Krthiga
2		3,4	11.15AM – 1.15 PM	Ellipse generation, Implementation style for fill styles: Scan line polygon filling algorithm, Boundary fill and Flood fill, Implementation methods for Antialiasing.	Mr. S. Parthiban
3		5.6	2 PM -4PM	2D transformation: Translation, Scaling, Rotation, Composite transformation, Reflection, Shearing, Raster Transformation	Mrs.S.Krthiga
DAY 2					
4	10.08.20	7,8	9AM -11AM	2D Viewing: Pipeline, Normalization and viewport transformation, 2D Clipping Algorithms: Point, Line, Polygon, Curve, Text	Mr. S. Parthiban
5		9,10,	11.15AM – 1.15 PM	3D Transformation: Translation, Scaling,	Mrs.S.Krthiga

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Puducherry - 605 110

				Rotation, Reflection, Shearing, 3D Viewing: Projection, Three-Dimensional Viewing concepts, 3D Viewing pipe line,	
6		11,12	2 PM -4PM	Normalization and viewport transformation, 2D Clipping Algorithms: Point, Line, Polygon, Curve, Text	Mr. S. Parthiban
DAY -3					
7	11.08.20	13,14	9AM -11AM	3D Transformation: Translation, Scaling, Rotation, Reflection, Shearing, 3D Viewing: Projection, Three-Dimensional Viewing concepts	Mrs.S.Krthiga
8		15,16	11.15AM – 1.15 PM	Three-Dimensional viewing coordinate parameters, Projection transformation: Parallel projection, Orthogonal projection: oblique, Perspective projection, View volume	Mr. S. Parthiban
9		17,18	2 PM -4PM	Color Models: Chromaticity Diagram, RGB model, YIQ model, CMY model, CMYK model, HSV model, HLS model,	Mrs.S.Krthiga
DAY -4					
10	12.08.20	19,20	9AM -11AM	Transformation between color models. Illumination models: Lighting Models Basic Illumination models:	Mr. S. Parthiban
11		21,22	11.15AM – 1.15 PM	Ambient Light: Diffusion Light: Specular reflection	Mrs.S.Krthiga
12		23,24	2 PM -4PM	Visible Surface	Mr. S. Parthiban

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PRINCIPAL
RAAK College of Engineering & Technology
No.1, Muthupillai Palayam Road,
Sulthanpet Post,
Puducherry - 605 110.

				Detection Methods: Back face detection	
DAY -5					
13	13.08.20	25,26	9AM -11AM	, Depth buffer method, A-Buffer method, Scan-line method	Mrs.S.Krthiga
14		27,28	11.15AM – 1.15 PM	BSP-Tree method, Area-subdivision method, Octree method	Mr. S. Parthiban
15		29,30	2 PM -4PM	, Ray-casting method, Curve and Line frame detection, Polygon rendering method	Mrs.S.Krthiga
ASESSMENT EXAM WILL BE CONDUCTED AFTER ONE WEEK OF COURSE COMPLETION *					

BREAK TIME: 11.00 TO 11.15 AM **LUNCH BREAK: 1.15 PM TO 2.00 PM**

COURSE DESIGNED BY
Ms. V. UVARANJANI

APPROVED BY
SKILL DEVELOPMENT TEAM

PRINCIPAL
Dr.S.SEENUVASAMURTHI

RAAK College of Engineering & Technology

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VALUE ADDED COURSE
2020-2021

Department of Electronics and communication Engineering

EVENT REPORT

Name of the Course 20ECE01-High speed system design(Online Mode)

Name of the Instructors: Mr. S. Parthiban, & Mrs.S.Krthiga

Year/ Branch: IV/ECE

Duration of Course: 30 Hours (09-08-2020 to 13-08-2020)

Assessment Date: 20.08.2020

Post Event Summary:

The course was inaugurated on 09-08-2020 at 9.30 A.M. by our respectable principal and sessions were continued as per the schedule. Students were enriched their knowledge by attending the course. Finally, the course concluded by vote of thanks.On 20.08.2020 assessment was conducted and feedbacks were collected from all the participants.

CO - Attainment:

CO1: To understand the concepts of High speed system design primitives and various graphics algorithms.

CO2: Design and demonstrate the 2D object transformation and viewing through graphicsprinciples.

CO3: To understand the various color models and comprehend the complexities of illumination invirtual scenes.

CO4: Have the ability to model the hidden surface and render the respective 3D objects so as toproject it on to the screen.

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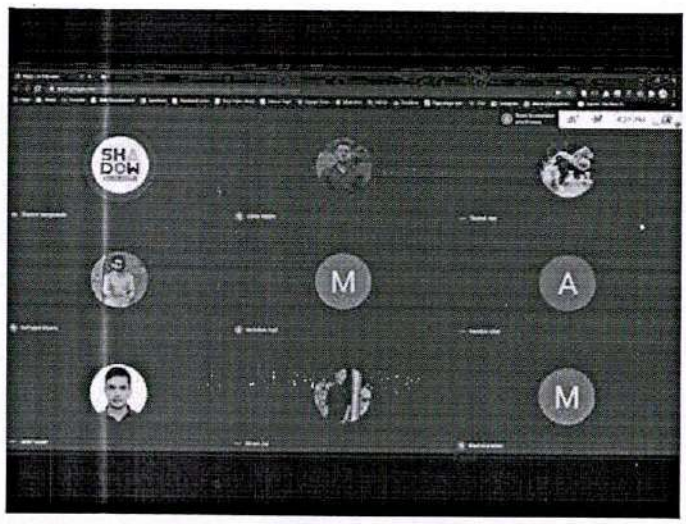
Value Added Course On High Speed System Design 2020-21



2D transformation: Translation, Scaling, Rotation, Composite transformation, Reflection, Shearing,Raster Transformation on 09.08.20

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03/08/2020

From

Mr. Joseph Selvaraj

Assistant Professor/ ECE

RAAK College of Engineering and Technology

Puducherry -110

To

The Principal

RAAK College of Engineering and Technology

Puducherry -110

Respected Sir,

Sub: Requisition for Approval to Conduct Skill Development program / Value added Course on **"20ECE02- Pattern recognition"** -reg.

This is to bring to your kind notice that the Skill Development Team is planning to conduct a Program on **"20ECE02- Pattern recognition"** for all the Third Year Electronics and communication Engineering students from 09-08-2020 to 13-08-2020.

The main focus of this program is to provide a better exposure to our students on the Pattern recognition

The syllabus and course plan structured are not listed in the Pondicherry University Curriculum and the same have been verified and approved by the Principal/HoD/Professors and Skill development team.

Hence, I kindly request you to approve event planned. The details and the necessary proofs are attached with this letter.

Thanking you,

Yours faithfully,

Mr. Joseph Selvaraj

AP/ECE

Dr. S. SEENUVASAMURTHI, M.E., Ph.D.
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RAAKCET/PRINCIPAL/CIR/AUG2020

04/08/2020

CIRCULAR

This is to inform that the Skill Devlopment Team is planning to conduct a value added course on **"20ECE02- Pattern recognition"** for all the Third Year ECE Department students from 09-08-2020 to 13-08-2020.studendts are asked to utilize this opportunity and improve their skills

PRINCIPAL

Circulation to:

1. All Students
2. All Faculty & Staff Members
3. All HoDs

Copy to:

1. All HoDs
2. Office

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING PRESENTS

VALUE ADDED COURSE ON

PATTERN RECOGNITION

2020-2021

DATE: 09/08/2020 to 13/08/2020

VENUE: RAAKCET

TIME: 09 am to 04 pm

Resource Person:

Mr. K. Vivek
Assistant Professor,
Christ Institute of Engg & Tech.

For Registration Contact:

Ms. R. Aarthi , AP/ ECE.,
9835485534.

HOD

Mr. Ayyapasamy

RAAK College of Engineering & Technology

PRINCIPAL

Dr. S. Seenuvasamurthi

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VALUE ADDED COURSES

2020-2021

Department of Electronics Communication Engineering

20ECE02- Pattern recognition

Syllabus

Duration: 30 hours

Course Objective:

- The subject aims to make the students to understand the mathematical approach for pattern recognition.

Course Outcome:

- Upon successful completion of the course students able to
- Summarize the various techniques involved in pattern recognition
- Identify the suitable pattern recognition techniques for the particular applications.
- Categorize the various pattern recognition techniques into supervised and unsupervised.
- Summarize the mixture models based pattern recognition techniques

Module 1: **(9 Hours)**

Polynomial curve fitting – The curse of dimensionality - Decision theory - Information theory - The beta distribution - Dirichlet distribution-Gaussian distribution-The exponent family: Maximum likelihood and sufficient statistics -Non-parametric method: kernel-density estimators - Nearest neigh bour methods.

Module 2: **(9 Hours)**

Linear models for regression and classification: Linear basis function models for regression - Bias variance decomposition-Bayesian linear regression-Discriminant functions - Fisher's linear discriminant analysis (LDA) - Principal Component Analysis (PCA) - Probabilistic generative model

Module 3: **(9 Hours)**

Probabilistic discriminative model.Kernel methods: Dual representations-Constructing kernels-Radial basis function networks-Gaussian process-Maximum margin classifier (Support Vector Machine) –Relevance Vector Machines-Kernel- PCA, Kernel-LDA.

Module 4: **(9 Hours)**

Mixture models: K-means clustering - Mixtures of Gaussian - Expectation-Maximization algorithm-Sequential models: Markov model, Hidden-Markov Model (HMM) - Linear Dynamical Systems(LDS).

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Puducherry - 605 110

(9 Hours)

Module 5:

Neural networks: Feed- forward Network functions-Network training - Error Back propagation The Hessian Matrix - Regularization in Neural Network - Mixture density networks – Bayesian Neural Networks

Course Designed By

Approved By

Principal

RAAK College of Engineering & Technology

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CO - ATTAINMENT MAPPING

Sl. No	Register Number	Student Name	CO1	CO2	CO3	CO4
1	18TC1201	ABITHA V	✓	✓	✓	✓
2	18TC1202	AGALYA B	✓	✓	✓	✓
3	18TC1203	ANITHA N	✓	✓	✓	✓
4	18TC1204	FOUSIA BEGAM Y	✓	✓	✓	✓
5	18TC1205	JOTHILAKSHMI A	✓	✓	✓	✓
6	18TC1206	KAMARUNISHA H	✓	✓	✓	✓
7	18TC1207	KAVITHA U	✓	✓	✓	✓
8	18TC1208	MATHIYARASI S	✓	✓	✓	✓
9	18TC1209	NASIRA BANU M	✓	✓	✓	✓
10	18TC1210	PAVITHRA S	✓	✓	✓	✓
11	18TC1211	PRIYADARSINI D	✓	✓	✓	✓
12	18TC1212	RAJALAKSHMI M	✓	✓	✓	✓
13	18TC1213	RAJESWARI P	✓	✓	✓	✓
14	18TC1214	SANGARI A	✓	✓	✓	✓
15	18TC1215	SANGEETHA K	✓	✓	✓	✓
16	18TC1216	SIVEDHA V	✓	✓	✓	✓
17	18TC1217	VASHUMATHY S	✓	✓	✓	✓
18	18TC1218	VISHNUPRIYA S	✓	✓	✓	✓

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VALUE ADDED COURSES

2020-2021

Department of Electronics Communication Engineering

20ECE02- Pattern recognition

COURSE PLAN

S.no	Date	Hours	Time	Topic	Faculty details
DAY -1					
1	09.08.20	1,2	9 AM -11 AM	Polynomial curve fitting – The curse of dimensionality - Decision theory - Information theory - The beta distribution	Mr. R. Rajendran, M.E.,& Mr. S. Parthiban
2		3,4	11.15 AM – 1.15 PM	Dirichlet distribution- Gaussian distribution- The exponent family: Maximum likelihood and sufficient statistics	Mr. R. Rajendran, M.E
3		5,6	2 PM -4 PM	Non-parametric method: kernel-density estimators - Nearest neigh bour methods.	Mr. S. Parthiban
DAY 2					
4	10.08.20	7,8	9 AM -11 AM	Linear models for regression and classification: Linear basis function models for regression	Mr. R. Rajendran, M.E
5		9,10	11.15AM – 1.15 PM	Bayesian linear regression- Discriminant functions - Fisher's linear discriminant analysis (LDA)	Mr. S. Parthiban
6		11,12	2 PM -4 PM	Principal Component Analysis (PCA) - Probabilistic generative model	Mr. R. Rajendran, M.E
DAY -3					
7	11.08.20	13,14	9 AM -11 AM	Probabilistic discriminative model.Kernel methods- Dual representations	Mr. S. Parthiban
8		15,16	11.15 AM – 1.15 PM	Radial basis function networks-Gaussian process-Maximum	Mr. R. Rajendran, M.E

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				margin classifier	
9		17,18	2 PM -4 PM	Relevance Vector Machines-Kernel- PCA, Kernel-LDA.	Mr. S. Parthiban
DAY -4					
10	12.08.20	19,20	9 AM -11 AM	Mixture models: K-means clustering - Mixtures of Gaussian	Mr. R. Rajendran, M.E
11		21,22	11.15 AM – 1.15 PM	- Expectation-Maximization algorithm-Sequential models:	Mr. S. Parthiban
12		23,24	2 PM -4 PM	Markov model, Hidden-Markov Model (HMM) - Linear Dynamical Systems(LDS).	Mr. R. Rajendran, M.E
DAY -5					
13	13.08.20	25,26	9 AM -11 AM	Neural networks: Feed- forward Network functions-Network training	Mr. S. Parthiban
14		27,28	11.15 AM – 1.15 PM	Error Back propagation The Hessian Matrix - Regularization in Neural Network	Mr. R. Rajendran, M.E
15		29,30	2 PM -4 PM	Mixture density networks – Bayesian Neural Networks	Mr. S. Parthiban
ASESSMENT EXAM WILL BE CONDUCTED AFTER ONE WEEK OF COURSE COMPLETION *					

BREAK TIME: 11.00 TO 11.15 AM **LUNCH BREAK: 1.15 PM TO 2.00 PM**

COURSE DESIGNED BY

Mr. JOSEPH SELVARAJ

APPROVED BY

SKILL DEVELOPMENT TEAM

PRINCIPAL

Dr.S.SEENUVASAMURTHI

RAAK College of Engineering & Technology

Dr. S. SEENUVASAMURTHI, M.E., Ph.D.
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VALUE ADDED COURSE

2020-2021

Department of Electronics Communication Engineering

EVENT REPORT

Name of the Course: 20ECE02-Pattern recognition(Online Mode)

Name of the Instructors: Mr. R. Rajendran, M.E & Mr. S. Parthiban

Year/ Branch: III/ECE

Duration of Course: 30 Hours (09-08-2020 to 13-08-2020)

Assessment Date: 20.08.2020

Post Event Summary:

The course was inaugurated on 09-08-2020 at 9.30 A.M. by our respectable principal and sessions were continued as per the schedule. Students were enriched their knowledge by attending the course. Finally, the course concluded by vote of thanks.

On 20.08.2020 assessment was conducted and feedbacks were collected from all the participants.

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CO1: Summarize the various techniques involved in pattern recognition

CO2: Identify the suitable pattern recognition techniques for the particular applications.

CO3: Categorize the various pattern recognition techniques into supervised and unsupervised.

CO4: Summarize the mixture models based pattern recognition techniques

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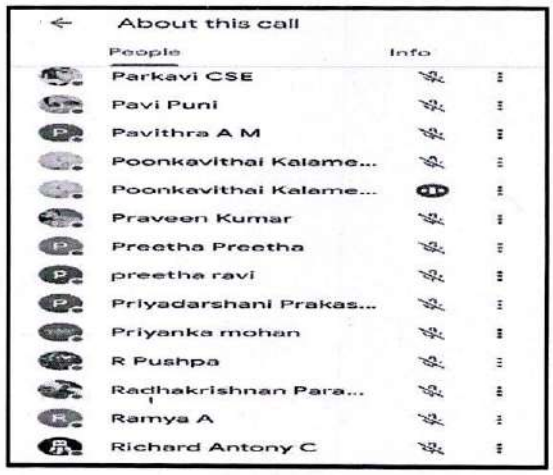
Value Added Course On Pattern recognition 2020-21



Bayesian linear regression-Discriminant functions on 10.08.20

RAAK College of Engineering & Technology

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COLLEGE OF ENGINEERING AND TECHNOLOGY

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03/08/2020

From

Ms. R. AARTHI

Assistant professor, ECE

RAAK College of Engineering and Technology

Puducherry -110

To

The principal

RAAK College of Engineering and Technology

Puducherry -110

Respected Sir,

Sub: Requisition for Approval to Conduct Skill Development program / Value added Course on **"20ECE03- Display systems"**-reg.

This is to bring to your kind notice that the Skill Development Team is planning to conduct a Program on **"20ECE03- Display systems"** for all the Second Year Electronics and Communication Engineering students from 09-08-2020 to 13-08-2020.

The main focus of this program is to provide a better exposure to our students on the Display systems.

The syllabus and course plan structured are not listed in the Pondicherry University Curriculum and the same have been verified and approved by the Principal/HoD/Professors and Skill development team.

Hence, I kindly request you to approve event planned. The details and the necessary proofs are attached with this letter.

Thanking you,

Yours faithfully,

R. Aarthi

Ms. R. Aarthi
AP/ECE

RAAK College of Engineering & Technology

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RAAKCET/PRINCIPAL/CIR/AUG2020 **04/08/2020**

CIRCULAR

This is to inform that the Skill Devlopment Team is planning to conduct a value added course on **"20ECE03- Display systems"** for all the Second Year ECE Department students from 09-08-2020 to 13-08-2020.Students are asked to utilize this opportunity and improve their skills.

PRINCIPAL

Circulation to:

1. All Students
2. All Faculty & Staff Members
3. All HoDs

Copy to:

1. All HoDs
2. Office

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING PRESENTS

VALUE ADDED COURSE ON

DISPLAY SYSTEM (ONLINE MODE)

2020-2021

DATE: 09/08/2020 to 13/08/2020

VENUE: RAAKCET

TIME: 09 am to 04 pm

Resource Person:

Mr. S. Prakash
Assistant Professor,
Christ Institute of Engg & Tech.

For Registration Contact:

Ms. V. Uvaranjani, AP/ ECE.,
9548346844.

HOD

Mr. Ayyapasamy

PRINCIPAL

Dr. S. Seenuvasamurthi

Dr. S. SEENUVASAMURTHI, M.E., Ph.D.
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VALUE ADDED COURSES
2020-2021
Department of Electronics and Communication Engineering
20ECE03- Display systems

Syllabus

Duration: 30 hours

Course Objective:

- To expose the students to the basics of the display systems and to illustrate the current design practices of the display systems.

Course Outcome:

Upon successful completion of the course students able to

CO1: Appreciate the technical requirement of different types of displays systems

CO2: Analyse the various low power lighting systems

CO3: Understand the operation of TFTs and LCD displays.

CO4: Analyze the various kinds of emissive displays

Module 1: **(9 Hours)**

Introduction to displays. Requirements of displays. Display technologies, CRT, Flat panel and advanced display technologies. Technical issues in displays.

Module 2: **(9 Hours)**

Head mounted displays. Displays less than and greater than 0.5 m diagonal. Low power and light emitting displays.

Module 3: **(9 Hours)**

Operation of TFTs and MIMS. LCDs, Brightness. Types of LCD displays.

Module 4: **(9 Hours)**

Emissive displays, ACTFEL, Plasma display and Field emission displays, operating principle and performance.

Module 5: **(9 Hours)**

Types of Displays: 3D, HDTV, LED, Touch screen.

Course Designed By

Approved By

Dr. S. SEENUVASAMURTHI, M.E., Ph.D.
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Principal

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CO - ATTAINMENT MAPPING

Sl. No	Register Number	Student Name	CO1	CO2	CO3	CO4
1	19TC1101	ABIRAMI S	✓	✓	✓	✓
2	19TC1102	BENINAL G	✓	✓	✓	✓
3	19TC1103	DINESH KUMAR V	✓	✓	✓	✓
4	19TC1104	GAUTHAM S	✓	✓	✓	✓
5	19TC1105	GOVINDARAJ K	✓	✓	✓	✓
6	19TC1106	I.KARTHESWARAN	✓	✓	✓	✓
7	19TC1107	I.MUTHURAMAN	✓	✓	✓	✓
8	19TC1108	KALAIVANAN M	✓	✓	✓	✓
9	19TC1109	KISHORE RAJAN N	✓	✓	✓	✓
10	19TC1110	NARAYANAN B	✓	✓	✓	✓
11	19TC1112	RAJALAKSHMI S	✓	✓	✓	✓
12	19TC1113	RESMINA FARVIN M	✓	✓	✓	✓
13	19TC1114	S.JEEVANANDAM	✓	✓	✓	✓
14	19TC1115	SATHISH N	✓	✓	✓	✓
15	19TC1116	SOORIYA D	✓	✓	✓	✓
16	19TC1117	SUGANYA M	✓	✓	✓	✓
17	19TC1118	YOGAA SUPARNA K P	✓	✓	✓	✓

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VALUE ADDED COURSES

2020-2021

Department of Electronics and Communication Engineering

20ECE03- Display systems

COURSE PLAN

S.no	Date	Hours	Time	Topic	Faculty details
DAY -1					
1	09.08.20	1,2	9 AM -11 AM	Introduction to displays. Requirements of displays	Mr.K.Vivek & Mr. R. Rajendran, M.E.,
2		3,4	11.15 AM – 1.15 PM	.Display technologies, CRT, Flat panel and.	Mr.K.Vivek
3		5.,	2 PM -4 PM	Advanced display technologies. Technical issues in displays	Mr. R. Rajendran, M.E.,
DAY 2					
4	10.08.20	7,8	9 AM -1 1AM	Head mounted displays.	Mr.K.Vivek
5		9,10,	11.15 AM – 1.15 PM	Displays less than and greater than 0.5 m diagonal.	Mr. R. Rajendran, M.E.,
6		11,12	2 PM -4PM	Low power displays	Mr.K.Vivek
DAY -3					
7	11.08.20	13,14	9 AM -11AM	light emitting displays	Mr. R. Rajendran, M.E.,
8		15,16	11.15 AM – 1.15 PM	Operation of TFTs and MIM	Mr.K.Vivek
9		17,18	2 PM -4 PM	LCDs, Brightness.	Mr. R. Rajendran, M.E.,
DAY -4					
10	12.08.20	19,20	9 AM -11AM	Types of LCD displays.	Mr.K.Vivek
11		21,22	11.15 AM – 1.15 PM	Emissive displays, ACTFEL	Mr. R. Rajendran, M.E.,
12		23,24	2 PM -4 PM	Plasma display	Mr.K.Vivek

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DAY -5					
13	13.08.20	25,26	9AM -11AM	Field emission displays	Mr. R. Rajendran, M.E.,
14		27,28	11.15AM – 1.15 PM	Types of Displays: 3D, HDTV	Mr.K.Vivek
15		29,30	2 PM -4 PM	. LED, Touch screen.	Mr. R. Rajendran, M.E.,
ASESSMENT EXAM WILL BE CONDUCTED AFTER ONE WEEK OF COURSE COMPLETION *					

BREAK TIME: 11.00 TO 11.15 AM **LUNCH BREAK: 1.15 PM TO 2.00 PM**

COURSE DESIGNED BY

Ms. R. AARTHI

APPROVED BY

SKILL DEVELOPMENT TEAM

PRINCIPAL

Dr.S.SEENUVASAMURTHI

RAAK College of Engineering & Technology

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VALUE ADDED COURSE
2020-2021

Department of Electronics and Communication Engineering

EVENT REPORT

Name of the Course: 20ECE03- Display systems(Online Mode)

Name of the Instructors: Mr.K.Vivek & Mr. R. Rajendran, M.E.,

Year/ Branch: II/ECE

Duration of Course: 30 Hours (09-08-2020 to 13-08-2020)

Assessment Date: 20.08.2020

Post Event Summary:

The course was inaugurated on 09-08-2020 at 9.30 A.M. by our respectable principal and sessions were continued as per the schedule. Students were enriched their knowledge by attending the course. Finally, the course concluded by vote of thanks.

On 20.08.2020 assessment was conducted and feedbacks were collected from all the participants.

CO - Attainment:

CO1: Appreciate the technical requirement of different types of displays systems

CO2: Analyze the various low power lighting systems

CO3: Understand the operation of TFTs and LCD displays.

CO4: Analyze the various kinds of emissive displays

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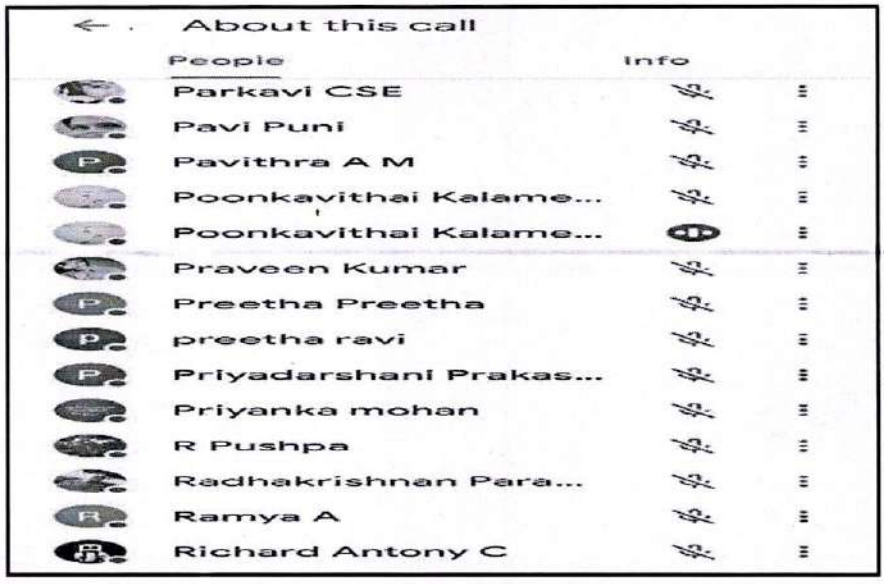
Value Added Course On Display systems 2020-21



schedule T- Good manufacturing practice of indian system of medicine on 13.08.20

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