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ATTAINMENTS

Course outcomes (CO) delincate the Knowledge and principles that students will possess upon successfully finishing a course. Based on CO attainment of all course components the PO/PSO assessment is finished at the end of each course. Each course has predefined learning objectives and corresponding assessment requirements. To give a quantitative assessment of the degree to which the program outcomes are met the course outcomes and the program outcomes are linked to each other. Through the process of mapping questions to COs and COs to POs and PSOs, the exam result at the conclusion of each semester is used to determine the student's level of achievement for POS and PSOs. The CO-PO is created subject Faculty.

CO ATTAINMENT

In order to track the evolution of the outcome, measuring procedures are developed for each CO. Direct and indirect assessment strategies are the two types available. The following sections provide information on the techniques and resources utilized to meet the course objectives.

Direct Method Includes:

- ➤ Theory Courses Internal and University exams
- Laboratory Courses Internal and University exams

Indirect Method Includes:

- Student Exit Survey
- ➤ Alumini survey
- Parent's Survey
- Employer's Survey

Guidelines are defined for the attainment of the outcomes



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The COs are evaluated based on the following criteria

- > External Examination: Marks secured in the University Examination.
- > The attainment level is calculated for the External examination by considering the students who achieve 55% and above
- > The specific CO attainment level is derived from the average marks secured by the students and the total marks of the particular CO.

The attainment levels are as follows:

Attainment Level

Level 3 - above 60%

Level 2 - 55% to 59%

Level 1 - 50% to 54%

Attainment of Programme Outcomes, Program specific outcomes:

Attainment of PO and PSO are calculated by the following tools

1. Direct Attainment

- Mark secured in University examination is considered
- > CO, PO mapping is done for all subjects
- > The attainment of POs in external assessment is done based on the great points obtained by students from the University semester examination
- > The attained level is considered for students scoring "c" grade and above in the endsemester exams
- > Articulation Matrix for all the subjects is calculated.
- > CO attainment table is computed for all the subjects for the complete year of passing.

PO attainment table is calculated by using the articulation Matrix and CO attainment table for all the subjects.

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- > Finally target attend table is calculated for checking the attainment level of students.
- > For direct attainment a weightage of 80% is calculated.

2. Indirect Attainment

For indirect attainment the following feedback survey are considered. It is computed for 20% weightage

- > Student Exit Survey
- Alumini survey
- Parent's Survey
- > Employer's Survey

This involves giving the students questioneries based on the POs and PSOs, rated on a scale:

0 - poor, 1- Satisfactory, 2- Good, 3- Very Good, 4- Excellent, 5- Outstanding

We calculate both the total weightage and maximum weightage for each PO and PSO. The Weightage for POs and PSOs is also derived from the percentage weightage

The attainment level consists of 80% from the direct method and 20% from the indirect method, representing the overall attainment for POs and PSOs.

The Department Advisory Committee (DAC) reviews the attainment every year and the suggestion on assessment questions and the effectiveness of the activities or offered by the expert members



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ATTAINMENT OF COURSE OUTCOMES

Batch: (2019 – 2023)

Regulation: (2013 - 2014)

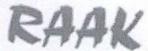
S. No	Course Code	Course Name	Appeared	Pass	Pass %	CO attainment	Target Level	Attainment status
1	T101	Mathematics - I	17	10	59	2	2.5	NA
2	T102	Physics	17	13	76	3	2.5	A
3	T103	Chemistry	17	7	41	0	2.5	NA
4	T110	Basic Civil and Mechanical Engineering	17	8	47	0	2.5	NA
5	T111	Engineering Mechanics	17	6	35	0	2.5	NA
6	T112	Communicative English	17	14	82	3	2.5	A
7	P104	Physics Laboratory	17	17	100	3	2.5	A
8	P105	Chemistry Laboratory	17	17	100	3	2.5	A
9	P106	Workshop Practice	17	17	100	3	2.5	A
10	T107	Mathematics - II	15	15	100	3	2.5	A
11	T108	Material Science	15	15	100	3	2.5	A
12	T109	Environmental Science	15	15	100	3	2.5	A
13	T104	Basic Electrical and Electronics Engineering	15	15	100	3	2.5	A



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	4 T10.	5 Engineering Thermodynamics	15	15	100	3	2.5	A
1:		- Togramming	15	15	100	3		
16	6 P101	Computer Programming Laboratory	15	15	100	3	2.5	A
17		Engineering Graphics	15	15	100	3		A
18	P103	Basic Electrical and Electronics Laboratory	15	15	100	3	2.5	A
19		NSS/NCC	15	15	100	3		
20	MA T3	- III	15	10	66.7	3	2.5	A
21	EC T32	- Engliceting	15	12	80	3	2.5	A
22	EC T33	Data Structures and object oriented programming	15	4	26.7	0	2.5	A NA
23	EC T34	Electronic Devices and Circuits	15	12	80	3	2.5	A
24	EC T35	Circuit Theory	15	13	92.9	3	2.5	
25	EC T36	Engineering Electromagnetics	15	10	71.4	3	2.5	A A
26	EC P31	Electrical Engineering Laboratory	15	15	100	3	2.5	A A
7	EC P32	Data Structures and object oriented programming Laboratory	15	15	100	3	2.5	A
3	EC P33	Electronic Devices and Circuits Laboratory	15	15	100	3 Phi	2.5	A

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	9 MA T	41 Mathematics - IV	15	15	100	3		
3	0 EC T4	2 Electronic Circuit and Analysis	15	4 3	100		2.5	A
3	1 EC T4.		13	15	100	3	2.5	A
32		o grano una systems	15	15	100	3	2.5	A
		control system	15	15	100	3	2.5	A
33		o surgano	15	15	100	3		
34	EC T46				100	3	2.5	A
35	EC D41	communication systems	15	15	100	3	2.5	A
		Electronic circuit design laboratory	15	14	93	3	2.5	A
36	EC P42	Digital circuits laboratory	15	14	93	3	2.5	A
37	EC P43	Communication laboratory - I	15	14	93	3	2.5	A
38	SP P44	Physical Education	15	15	100	3		
39	MA T51	Probability and Random Processes	14	11	79	3	2.5	A
40	EC T52	Data Communication Network	14	14	100	3	2.5	A A
41	EC T53	Microprocessors and Microcontrollers	14	9	64	3	2.5	A
12	EC T54	System design using integrated circuits	14	14	100	3	2.5	
.3		Transmission lines and wave guides	14	14	100	Shim	2.5	A A

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4	4 EC EC	O2 Consumer Electronics	14	13	93	3		
4	5 EC PS	Microprocessors and Microcontroller Laboratory	14	14	100	3	2.5	A
46	EC P5	- Joseph design using					2.0	A
		integrated circuits Laboratory	14	14	100	3	2.5	A
47		Transmission Lines Laboratory	14	14	100	3	2.5	A
48		- Tonelency - 1	14	14	100	3	2.5	A
	EC T61	- gran Communication	14	14	100	3	2.5	A
50	EC T62	cress communication	14	14	100	3	2.5	
51	EC T63	Digital Signal Processing	14	13	93	3	2.5	A
52	EC T64	Antennas and wave propagation	14	13	93	3	2.5	A
53	EC E06	VLSI design	14	14	100	3		
54	EC P61	Communication laboratory - II	14	14	100	3	2.5	A A
5	EC P62	Computer Networks laboratory	14	14	100	3	2.5	A
6	EC P63	Digital Signal Processing laboratory	14	14	100	3	2.5	A A
7	HS PS4	General Proficiency – II	14	14	100	30		
	EC T71	Microwave and Optical Engineering	14	13	93	3	2.5 UR ² -\$1, M.E.,	A Ph.C

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59	EC T7	2 Embedded Systems	14	14	100	3	2.5	
60	EC E1	4 Cryptography and Network Security	14	14	100	3	2.5	A
61		Communication	14	14	100	3	2.5	A
62	EC P71	laboratory – III	14	14	100	3	2.5	A
63	EC P72	Embedded Systems laboratory	14	14	100	3	2.5	A
64	EC P73		14	14	100	3	2.5	A
65	EC P74	Industrial Visit/Training	14	14	100	3	2.5	A
66	EC PW7	Jacobs Work - 1	14	14	100	3	2.5	A
67 68	EC T81	Professional Ethics	14	14	100	3	2.5	A
00	EC T82	Industrial Management and Engineering Economics	14	14	100	3	2.5	A
9	EC E16	Satellite Communication systems	14	14	100	3	2.5	A
0	EC E20	Cellular Mobile Communication	14	14	100	3	2.5	A
		Advanced Communication Laboratory	14	14	100	3	2.5	A
		Comprehensive Viva	14	14	100	3	2.5	A
	EC PW8	Project Work - II	14	14	100	3	2.5	A



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Course Articulation Matrix

Semester	Sub	Subject	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
	T101	Mathematics - I	1.77	1.77	1.77	1.77	100		-	-	- 2			1.77	1.77	1.77	1.77
	T102	Physics	2.43	2.43	2.43	2.43	-	2.43	-	-	-	- 1	W-1	2.43	2.43	2.43	2.43
	T103	Chemistry	1.74	1.74	1.74	-	14-11	1.74	1.74		-	-	-	1.74	1.74	1.74	1.74
	T110	Basic Civil and Mechanical Engineering	1.7	1.7	1.7	1.7	-	-	-	9-1	-		- 4	1.7	1.7	1.7	1.14
	T111	Engineering Mechanics	1.53	1.53	-	1.53	-	1.53	-	-		1.53	-	1.53	-	-	1.53
	T112	Communicative English		- 20	-	-		-	-	-	2.58	2.58		2.58	-	-	2.58
	P104	Physics Laboratory	3	_		3		3	3		3	3		3	3	3	3
	P105	Chemistry Laboratory	3	-	-	-	-	3	3		3	3	Vin	3	3	-	3

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	P106	Workshop Practice	_	-	3	-	_	-		-	-	-	-		-	-	1
	T107	Mathematics - II	2.61	2.61	2.61	2.61	-	-	2.61	-	-	4.4	14 -111	2.61	2.61	2.61	2.61
	T108	Material Science	2.49	2.49	-	-	2.49	2.49	-	-	-	-	2.49	2.49	2.49	2.49	2.49
	T109	Environmental Science	2.61	-	-	-	-	2.61	2.61	-		-	2.61	26.1	2.61	-	-
	T104	Basic Electrical and Electronics Engineering	2.43	2.43	2.43	- 2	-	2.43	2.43	-	-	-	2.43	2.43	2.43	2.43	2.43
	T105	Engineering Thermodynamics	2.43	2.43	2.43	2.43			7-7	-		-	-		2.43		1.62
II	T106	Computer Programming	2.43	2.43	2.43	2.43	2.43	2.43	2.43	2.43	2.43	2.43	2.43	2.43	2.43	2.43	2.43
	P101	Computer Programming Laboratory	3	3	3	3	3	3	-	- 1	3		3	3	3	3	3
	P102	Engineering Graphics	3	3	-	-		-	-	-	-	-	7-11	-	1 - 1	-	-
	P103	Basic Electrical and Electronics Laboratory	3	3	-			3	3	-	3	3	3	3	3	3	2
	P107	NSS/NCC	3	-	-	-	3	3	3	3	3	3	311	3	-	-	



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MA T31	Mathematics - III	2.5	2.5	2.5	2.5	-	-	2.5		-	- T	-	2.5	2.5	2.5	2.5
EC T32	Electrical Engineering	2.55	2.55	2.55	-	-	2.55	2.55	-	-	-	2.55	2.55	2.55	2.55	1.53
EC T33	Data Structures and object oriented programming	1.59	1.59	1.59	1.59	1.59	1.59	-		-	-	1.59	1.59	1.59	1.59	0.53
EC T34	Electronic Devices and Circuits	2.34	2.34	2.03	2.03	-	-	i		-	-		1.9	2.34	2.03	1.56
EC T35	Circuit Theory	2.52	2.52	2.52	2.52	- 1	-	-	-	-	-	-	1.68	2.52	2.52	1.01
EC T36	Engineering Electromagnetics	2.52	2.52	2.2	2.01	-	-	2.52		-	-	Jul -	2.01	-	2.01	1.34
EC P31	Electrical Engineering Laboratory	3	3		-	-	3	3		3	3	3	3	3	3	1.6
EC P32	Data Structures and object oriented programming Laboratory	3	3	3	3	3	3			3		3	3	3	3	3
EC P33	Electronic Devices and Circuits Laboratory	3	3	3	3	3	in-1	-	3	-	3	J.	3	3	3	1.6
	EC T34 EC T36 EC P31	EC T32 Electrical Engineering EC T33 Data Structures and object oriented programming EC T34 Electronic Devices and Circuits EC T35 Circuit Theory EC T36 Engineering Electromagnetics EC P31 Electrical Engineering Laboratory EC P32 Data Structures and object oriented programming Laboratory EC P33 Electronic Devices and	EC T32 Electrical Engineering 2.55 EC T33 Data Structures and object oriented programming 1.59 EC T34 Electronic Devices and Circuits 2.34 EC T35 Circuit Theory 2.52 EC T36 Engineering Electromagnetics 2.52 EC P31 Electrical Engineering Laboratory 3 EC P32 Data Structures and object oriented programming Laboratory 3 EC P33 Electronic Devices and Circuits Laboratory 3	EC T32 Electrical Engineering 2.55 2.55 EC T33 Data Structures and object oriented programming 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 EC T35 Circuit Theory 2.52 2.52 EC T36 Engineering Electromagnetics 2.52 2.52 EC P31 Electrical Engineering Laboratory 3 3 EC P32 Data Structures and object oriented programming Laboratory 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3	EC T32 Electrical Engineering 2.55 2.55 EC T33 Data Structures and object oriented programming 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 2.03 EC T35 Circuit Theory 2.52 2.52 2.52 EC T36 Engineering Electromagnetics 2.52 2.52 2.52 EC P31 Electrical Engineering Laboratory 3 3 EC P32 Data Structures and object oriented programming Laboratory 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3	EC T32 Electrical Engineering 2.55 2.55 2.55 - EC T32 Data Structures and object oriented programming 1.59 1.59 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 2.03 2.03 EC T35 Circuit Theory 2.52 2.52 2.52 2.52 EC T36 Engineering Electromagnetics 2.52 2.52 2.52 2.01 EC P31 Electrical Engineering Laboratory 3 3 3 3 EC P32 Data Structures and object oriented programming Laboratory 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3	EC T32 Electrical Engineering 2.55 2.55 2.55 - - EC T32 Data Structures and object oriented programming 1.59 1.59 1.59 1.59 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 2.03 2.03 - EC T35 Circuit Theory 2.52 2.52 2.52 2.52 - EC T36 Engineering Electromagnetics 2.52 2.52 2.22 2.01 EC P31 Electrical Engineering Laboratory 3 3 3 3 3 EC P32 Data Structures and object oriented programming Laboratory 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P34 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P35 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P36 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P37 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P38 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P38 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P39 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits L	EC T32 Electrical Engineering 2.55 2.55 2.55 - - 2.55 EC T32 Data Structures and object oriented programming 1.59 1.59 1.59 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 2.03 2.03 - - EC T35 Circuit Theory 2.52 2.52 2.52 2.52 - - EC T36 Engineering Electromagnetics 2.52 2.52 2.2 2.01 EC P31 Electrical Engineering Laboratory 3 3 3 3 3 3 EC P32 Data Structures and object oriented programming Laboratory 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P34 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P35 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P36 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P37 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P38 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P39 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3	EC T32 Electrical Engineering 2.55 2.55 2.55 - - 2.55 2.55 EC T33 Data Structures and object oriented programming 1.59 1.59 1.59 1.59 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 2.03 2.03 - - - EC T35 Circuit Theory 2.52 2.52 2.52 2.52 - - - EC T36 Engineering Electromagnetics 2.52 2.52 2.2 2.01 - - 2.52 EC P31 Electrical Engineering Laboratory 3 3 3 3 3 3 EC P32 Data Structures and object oriented programming Laboratory 3 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P34 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P35 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P36 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P37 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P38 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P38 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P38 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P39 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 EC P30 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 EC P30 Electronic Device	EC T32 Electrical Engineering 2.55 2.55 2.55 - 2.55 2.55 - EC T32 Electrical Engineering 2.55 2.55 2.55 - EC T33 Data Structures and object oriented programming 1.59 1.59 1.59 1.59 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 2.03 2.03 - - EC T35 Circuit Theory 2.52 2.52 2.52 2.52 - - EC T36 Engineering Electromagnetics 2.52 2.52 2.22 2.01 - 2.52 EC P31 Electrical Engineering Laboratory 3 3 3 3 3 3 3 EC P32 Data Structures and object oriented programming Laboratory 3 3 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 3 3	EC T32 Electrical Engineering 2.55 2.55 2.55 - - 2.55 2.55 - - EC T32 Data Structures and object oriented programming 1.59 1.59 1.59 1.59 1.59 1.59 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 2.03 2.03 - - - - - EC T35 Circuit Theory 2.52 2.52 2.52 2.52 - - - - EC T36 Engineering Electromagnetics 2.52 2.52 2.52 2.01 - 2.52 - - - EC P31 Electrical Engineering Laboratory 3 3 3 3 3 3 3 3 EC P32 Data Structures and object oriented programming Laboratory 3 3 3 3 3 3 3 3 EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 3 - - EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 - - EC P33 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 3 - - EC P34 Electronic Devices and Circuits Laboratory 3 3 3 3 3 3 3 3 3	EC T32 Electrical Engineering 2.55 2.55 2.55 - - 2.55 2.55 - - - - - - - - -	EC T32 Electrical Engineering 2.55 2.55 2.55 - - 2.55 2.55 - - 2.55 2.55 EC T33 Data Structures and object oriented programming 1.59 1.59 1.59 1.59 1.59 1.59 1.59 1.59 EC T34 Electronic Devices and Circuits 2.34 2.34 2.03 2.03 - - - - - - - - EC T35 Circuit Theory 2.52 2.52 2.52 2.52 - - - - - - - EC T36 Engineering Electromagnetics 2.52 2.52 2.20 - - - - - - - EC P31 Electrical Engineering Laboratory 3 3 3 3 3 3 3 3 3	EC T32 Electrical Engineering 2.55 2.55 2.55 - - 2.55 2.55 - - 2.55 2.55	EC T32 Electrical Engineering 2.55 2.55 2.55 - - 2.55 2.55 2.55	T31

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	MA T41	Mathematics - IV	2.23	2.58	2.4	2.06	-		-	-	1	-	-	2.4	2.23	2.09	2.23
	EC T42	Electronic Circuit and Analysis	2.04	2.04	1.42	2.04	•	-		_	_	-	1.42	0.78	2.04	1.73	1.57
	EC T43	Signals and Systems	2.25	2.25	2.25	2.25	- 1	-	-	-1	-	-	- 1	0.9	2.25	2.25	2.25
	EC T44	Linear and digital control system	2.46	2.46	2.13	2.29	0.82	-		-	N.	•	-	1.14	1.31	1.14	0.82
IV	EC T45	Digital circuits	2.67	2.67	2.67	2.67	-			-		-	- 1-	2.67	2.67	1.78	2.67
ı	EC T46	Electronic communication systems	2.49	2.49	2.49	2.49	-	-	-	-	-		-	1.66	1.83	1.99	2.49
	EC P41	Electronic circuit design laboratory	2	2		-	-	-	-	-	-/-	3	_	-	2	_	2
	EC P42	Digital circuits laboratory	3	2	2	2	7-	- 7	4	-			2	1	3	2	3
	EC P43	Communication laboratory - I	2	3	2	2	2		-	-	-	2	2	-	2	3	3
	SP P44	Physical Education	-	-		-	- II	2	W -	2	2	3	FAN	3	-	-	1



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	MA T51	Probability and Random Processes	1.95	2.1	2.1	1.2		1	X (# 2			Test 1					
	EC T52	2 Data Communication			2.1	1.2	-	-	-	-	-			1.95	1.6		
		Network	2.26	2.1	1.94	1.94	1.94	4 1.29	-		-			1.55	1.5	1.5	1.:
	EC T53	Microprocessors and Microcontrollers	1.000				1.5	1.25					1.77	2.1	2.1	2.1	0.2
	EC T54		1.9825	1.97	1.82	2.21	-	1.52	-	_	_	_	1.50				-
V		integrated circuits	2.4	2.26	2.26	2.42	2.			1 1 1 1			1.52	2.21	2.21	2.21	2.28
E	EC T55	Transmission lines and			2.20	2.42	2.1			1.93	-	1.3	1.78	2.26	2.26	0.97	1.00
	EC E02	wave guides	1.78	1.62	0.97	0.64	-	0.97	0.97	1.78	13-31	0.0				0.57	1.62
		Consumer Electronics	2.22	2.22	2.38	2.22	2.00			1.76		0.97	-	1.45		0.97	1.62
	EC P51	Microprocessors and Microcontroller	TEN		2.50	2.22	2.22	2.38	2.22	-	-	-	-	2.06	2.38	2.38	246
		Laboratory	2.8	1.8	1.8	-	2.6	-	_				The A			2.36	
I	EC P52	System design using										-	-	1-1-	2.6	-	1.6
		integrated circuits Laboratory	2.8	1.8	2	2.4	2.4		22				0.0				
ATT.			AK Colles	C. A.	771				2.2	-	2.8	-	His	1.8	1.8	_	



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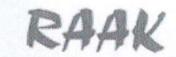
	EC P53	Networks and Transmission Lines Laboratory	2.8	2.8	-	_	-	0.8	-	-	-	0.8	-	-	-	1.8	- I
	HS P 54	General Proficiency - I		-	-	-	-	-	-	-	2.8	2.8	-	2.8	-	2.6	-
	EC T61	Digital Communication	2.72	2.52	2.72	2.52	-	-		-	- 1-	-	-	2.72	2.16	2.52	2.72
	EC T62	Wireless communication	2.58	1.2	1.89	-	-	-	-	-	-	-	-	1.2	-	1.89	2.58
	EC T63	Digital Signal Processing	2.6	2.6	2.41	2.41	-	-		-	-	g - 1.	4.5	2.41	2.41	2.41	2.8
	EC T64	Antennas and wave propagation	2.55	2.38	2.38	1.87	-	-	-	1	-	-	-	1.87	-	1.87	2.55
VI	EC E06	VLSI design	2.64	2.64	2.64	2.64	-	-		-	-	-	115	2.64	2.64	0.88	2.64
	EC P61	Communication laboratory - II	3	-	-	-	3	-	-	-	2	2	-	-		2	3
	EC P62	Computer Networks laboratory	3	3	3	2	2	2	•	-	-		2	2	2	3	3
	EC P63	Digital Signal Processing laboratory	3	-	-	-	3	-		ē	2	2	S	(m)		2	3



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	HS PS4	General Proficiency – II			-	-	-	-	-	-	3	3	-	3	-	3	3
	EC T71	Microwave and Optical Engineering	2.64	2.28	2.64	1.6	-		0.88	-		-	0.88	2.64	_	2.64	1.30
	EC T72	Embedded Systems	2.43	2.43	2.26	2.1	2.43	2.26	12			-	-	1.9	1.9	1.9	
	EC E14	Cryptography and Network Security	2.43	2.1	2.43	1.29	-	1.45	0.32	1.29	-	0.97	-	2.43	1.13	2.43	0.9
	EC E15	Spread Spectrum Communication	2.55	2.3	2.04	1.7		1.36	0.51	1.7		1.7	-	2.04	2.04	1.7	1.7
VII	EC P71	Communication laboratory – III	3	2	1			1				2.8		-		2.8	1
	EC P72	Embedded Systems laboratory	2.4	2.2	2	1.4	1	-		-		1.8			2.6	2	1
	EC P73	Seminar	2.8	<u>-</u>	12	-/-	-			_	2.8	2.8	2.8				
	EC P74	Industrial Visit/Training	3	_		_	-	2	2	2	2	2		2	1.8	2.6	2.8
	EC PW7	Project Work - I	3	3	3	3	3	3	3	3	3	1	Mam		2.6	2.6	2.6



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	EC T81	Professional Ethics	-		1-1	_	-	_	_	2.4	2.6	2.4	2.4	2.4			
	EC T82	Industrial Management and Engineering Economics	2.02	0.93	0.78	0.78	0.93	_		-	-	-	-	1.4	1.25	0.93	1.54
	EC E16	Satellite Communication systems	1.84	0.92	-	-	-	0.92	1.84	0.92	_	-	-	1.84		0.92	0.80
VIII	EC E20	Cellular Mobile Communication	2.11	1.66	1.81	1.36	0.75	-	-	-	-	-	_	_	1.51	-	0.4:
	EC P81	Advanced Communication Laboratory	3	1.8	-		2.8			0.8	-	2.8	-	-	4100	2.6	3
	EC P82	Comprehensive Viva	3	10-1		-	-	- 1	1-6	2.4	1.8	2.8		1.8	_	and the	2
	EC PW8	Project Work - II	3	2.6	2.8	2.8	3	2.8	2.8	2.8	2.8	2.8	2.6	3	2.4	2.4	3
	AVI	ERAGE	2.52	2.3	2.23	2.13	2.3	2.2	2.22	2.1	2.6	2.4	2.8/	2.6	2.3	2.2	2.1



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PO Attainment Direct Method

POs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
Average Value	2.52	2.3	2.23	2.13	2.3	2.2	2.22	2.1	2.6	2.4	2.3	2.6	2.3	2.2	2.1
80 % of average value	2.02	1.84	1.78	1.70	1.84	1.76	1.78	1.68	2.1	1.92	1.84	2.1	1.84	1.76	1.68

PO Attainment Indirect Method

POs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
Average Value	2.63	2.57	2.69	2.5	2.58	2.5	2.83	2.63	2.74	2.58	2.78	2.69	2.53	2.73	2.58
20 % of average value	0.53	0.51	0.54	0.5	0.52	0.5	0.57	0.53	0.55	0.52	0.56	0.54	0.51	0.55	0.516

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Overall Attainment

POs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
Direct 80%	2.02	1.84	1.78	1.70	1.84	1.76	1.78	1.68	2.1	1.92	1.84	2.1	1.84	1.76	1.68
Indirect 20%	0.53	0.51	0.54	0.5	0.52	0.5	0.57	0.53	0.55	0.52	0.56	0.54	0.51	0.55	0.516
Overall Attainment	2.55	2.35	2.32	2.2	2.36	2.26	2.35	2.21	2.65	2.44	2.4	2.64	2.35	2.31	2.2

PO - PSOs Target/ Attained Overall Attainment

2019 - 2023	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
Attained	2.55	2.35	2.32	2.2	2.36	2.26	2.35	2.21	2.65	2.44	2.4	2.64	2.35	2.31	2.2
Target	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2
Remarks	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A

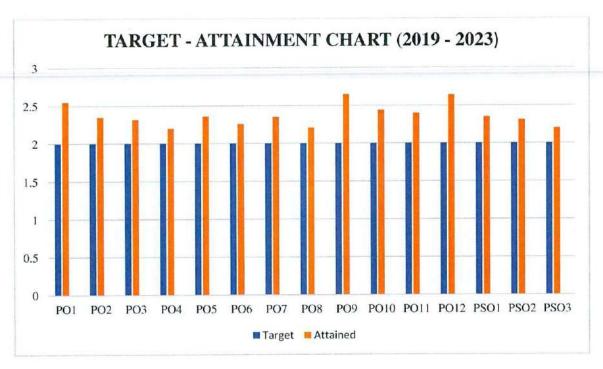
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING INDIRECT ATTAINTMENT OF POS AND PSOS

Student Exit Survey

Exit Survey to measure the attainment of Pos and PSOs by indirect method, exit survey given by 14 students out of 14 is taken. The survey is taken on a 5-point scale reflecting the graduate attributes, which measures the Attainment of Pos and PSOs. In the table below, the consolidated report of the exit survey is given:

S.NO	Assessment of Abilities, Skills and Attributes Acquired	0	1	2	3	4	5
PO1	Basic knowledge in mathematics, science, engineering and humanities.	0	1	1	2	6	4
PO2	Ability to identify, design, analyze and solve computer engineering	0	1	2	1	4	6
PO3	Design/develop of complex engineering problems and their solutions	0	0	2	3	3	6
PO4	Use of research-based knowledge and research methods	0	1	2	1	3	7
PO5	Demonstrate the ability of advanced technologies to solve real time problems	0	0	1	1	5	7
PO6	Awareness to apply engineering solutions in global, national and societal contexts.	0	1	2	1	7	3
PO7	Understanding professional engineering solutions in societal and environmental contexts.	0	0	1	2	4	7
PO8	Understand of professional and ethical responsibilities.	0	1	1	2	6	4
PO9	Ability to function as an effective member in multi-disciplinary teams	0	0	1	2	5	6
PO10	Proficiency in English language in both communication and technical Forms	0	0	2	0	4	8
PO11	Demonstrate the ability to choose and apply appropriate resources	0	0	1	2	4	7



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Capable of self-education and a clear understanding of the value of updating their professional knowledge to engage in lifelong learning.	0	1	1	3	5	4
Exhibiting attitude for continuous learning and deliver efficient solutions for emerging challengers in the computation domain.	0	1	2	1	7	3
Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry	0	0	1	2	4	7
Research and Development	0	1	1	3	5	4
	Exhibiting attitude for continuous learning and deliver efficient solutions for emerging challengers in the computation domain. Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry	Exhibiting attitude for continuous learning and deliver efficient solutions for emerging challengers in the computation domain. Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry Research and Development	Exhibiting attitude for continuous learning and deliver efficient solutions for emerging challengers in the computation domain. Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry Research and Development	Exhibiting attitude for continuous learning and deliver efficient solutions for emerging challengers in the computation domain. Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry Research and Development	Exhibiting attitude for continuous learning and deliver efficient solutions for emerging challengers in the computation domain. Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry Research and Development	Exhibiting attitude for continuous learning and deliver efficient solutions for emerging challengers in the computation domain. Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry Research and Development

The survey of those 14-point graduate attributes is mapped suitably to measure the attainment of POs and PSOs. The consolidated report of the exit survey in percentage is mapped against POs and PSOs presented in the following table.

Exit	PO	PS	PS	PS											
Survey	1	2	3	4	5	6	7	8	9	10	11	12	OI	O2	O3
Attain ment	86	79	86	79	93	79	93	86	93	86	93	86	79	93	86

The five point scale exit survey report is then converted into 3-point scale and mapped against POs and PSOs presented in the following table.

Exit	PO	PS	PS	PS											
Survey	1	2	3	4	5	6	7	8	9	10	11	12	O1	O2	O3
Attain ment	2.5	2.3	2.5	2.3	2.7	2.3	2.7	2.5	2.7	2.5	2.7	2.5	2.3	2.7	2.5



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Alumini Feedback Survey

Alumini Feedback Survey to measure the attainment of POs and PSOs by indirect method, alumini survey given by 14 students out of 14 is taken. The survey is taken on a 5-point scale reflecting the graduate attributes, which measures the attainment of POs and PSOs. In the table below, the consolidated report of the exit survey is given:

The survey of those 14-point graduate attributes is mapped suitably to measure the attainment of POs and PSOs.

S.No	Assessment of abilities	, skills and attributes	0	1	2	3	4	5
1	Engineering Knowledge	Technical Skill	. 0	1	1	2	4	6
2	Problem Analysis	Creativity	0	0	1	2	5	6
3	Design/Development of solutions	Analytical Thinking	0	1	1	2	6	4
4	Conduct investigation of Complex problems	Analytical Thinking	0	1	2	1	4	6
5	Modern tool usage		0	0	2	3	3	6
6	Engineer and Society	Technical Skill	0	1	2	1	3	7
7	Environment and sustainability		0	0	1	1	5	7
8	Ethics	Decision Making	0	0	1	3	6	4
9	Individual and team work	Team Collaboration	0	0	2	I	4	7
		Verbal Communication	0	0	2	3	5	4
10	Communication	Written Communication	0	1	0	2	4	7
		Presentation Skill	0	1	1	1	5	6



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11	Project Management and finance	Motivation and Guidance	0	0	1	1	4	8
12	Lifelong Learning		0	1	1	2	6	4
13	Core Knowledge	Professional Development	0	0	2	2	4	6
14	Technical Proficiency	Career Advancement and Satisfaction	0	0	1	1	5	7
15	Research and Development		0	1	1	1	5	6

The consolidated report of the alumini survey in percentage is mapped against POs and PSOs presented in the following table

Alumini's	PO	PS	PS	PS											
Survey	1	2	3	4	5	6	7	8	9	10	11	12	O1	O2	O3
Attainment	86	93	86	78	86	78	93	93	86	86	93	86	86	93	86

The Five point scale alumini survey is then into 3-point scale and mapped against POs and PSOs presented in the following

Alumini's Survey	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PS O1	PS O2	PS O3
	2.5	2.9	2.5	2.3	2.5	2.3	2.9	2.9	2.5	2.5	2.9	2.5	2.5	2.9	2.5
Attainment	8	7	8	4	8	4	7	7	8	8	7	8	8	7	8

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Parent's survey

Parent's survey to measure the attainment of POs and PSOs by indirect method parent's survey given by 14 out of 14 of the 2023 passed out students is taken. The survey is taken on a 5-point scale reflecting the graduate attributes, which measures the attainment of POs and PSOs. In the table below, in the consolidated report of the exit survey is given:

Programme Satisfaction:

S.NO	Assessment of abilities, skills and attributes acquired	0	1	2	. 3	4	5
1	Overall Satisfaction with the program	0	1	1	3	5	4
2	Quality of Academic Instruction	0	1	2	1	7	3
3	Relevance of Curriculum to Industry Needs	0	0	1	2	4	7
4	Availability of Resources (labs , materials etc)	0	1	1	2	6	4
5	Communication from institution to parents	0	0	2	4	4	4

Specific Program Aspects:

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
6	Availability and knowledge of subject matter	0	1	2	1	4	6
7	Facility: Adequacy of Laboratories, access to technology, study space	0	0	1	2	6	5

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8	Students Support: Counselling Services, Career guidance, academic advising	0	1	1	0	5	7
9	Industry exposure: Internship Opportunities, Industry Guest Lectures and networking events	0	0	1	2	3	8
10	Preparation for future	0	1	1	1	4	7
11	Aptitude Training and placement	0	1	1	0	6	6
12	Communication	0	0	1	2	5	6
13	Recommendation	0	0	2	0	4	8
14	Research and Development	0	0	1	2	5	6
15	Overall	0	0	1	2	4	7

The survey of those 14-point based on graduate attributes is mapped suitably to measure the attainment of POs and PSOs. The consolidated report of the parents survey in percentage is mapped against POs and PSOs presented in the following table

Parents	PO 7	PO	PO	PO	PO	PO	PS	PS	PS						
Survey	I	2	3	4	5	6		8	9	10	11	12	OI	O2	O3
Attainm ent	86	79	93	86	86	79	93	86	93	86	86	93	86	93	93

The Five point scale alumini survey is then into 3-point scale and mapped against POs and PSOs presented in the following

Parents Survey	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PS O1	PS O2	PS O3
	2.5	2.3	2.7	2.5	2.5	2.3	2.7	2.5	2.7	2.5	2.5	2.7	2.5	2.7	2.7
Attainment	8	7	9	8	8	7	9	8	9	8	8	9 81	28	9	9



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Employers Survey

Employer survey to measure the attainment of POs and PSOs by indirect method employer survey given by 14 out of 14 of the 2023 passed out students is taken. The survey is taken on a 5-point scale reflecting the graduate attributes, which measures the attainment of POs and PSOs. In the table below, in the consolidated report of the exit survey is given:

Assessment of Abilities, Skills and Attributes acquired	0	1	2	3	4	5
Technical Skills						
Knowledge of Core Engineering Concepts	0	0	1	2	4	7
Application of Technical Knowledge	0	0	2	0	4	8
Problem-Solving Abilities	0	0	1	2	5	6
Communication Skil	ls					
Communication (Verbal and Written)	0	1	1	3	5	4
Teamwork and Collaboration	0	1	2	1	7	3
Adaptability	0	0	1	2	4	7
Practical Experience	2					
Relevance of Academic Knowledge to the Workplace	0	0	1	3	6	4
Ability to Apply Theory to Practical Situations	0	1	2	1	4	6
	Technical Skills Knowledge of Core Engineering Concepts Application of Technical Knowledge Problem-Solving Abilities Communication Skill Communication (Verbal and Written) Teamwork and Collaboration Adaptability Practical Experience Relevance of Academic Knowledge to the Workplace Ability to Apply Theory to Practical	Technical Skills Knowledge of Core Engineering Concepts Application of Technical Knowledge Problem-Solving Abilities Communication Skills Communication (Verbal and Written) Teamwork and Collaboration Adaptability O Practical Experience Relevance of Academic Knowledge to the Workplace Ability to Apply Theory to Practical 0	Technical Skills Knowledge of Core Engineering Concepts Application of Technical Knowledge Problem-Solving Abilities Communication Skills Communication (Verbal and Written) Teamwork and Collaboration Adaptability Practical Experience Relevance of Academic Knowledge to the Workplace Ability to Apply Theory to Practical 0 1	Technical Skills Knowledge of Core Engineering Concepts Application of Technical Knowledge Problem-Solving Abilities Communication Skills Communication (Verbal and Written) Teamwork and Collaboration O 1 2 Adaptability Practical Experience Relevance of Academic Knowledge to the Workplace Ability to Apply Theory to Practical O 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Technical Skills	Technical Skills



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9	Overall Work Performance	0	0	1	2	6	5
	Specific Skills Assess	ment		1			
10	Project Management	0	0	2	2	4	6
11	Technical Proficiency	0	0	1	1	5	7
12	Professionalism	0	1	0	2	4	7
13	Program Contribution to Workplace Success	0	1	1	1	5	6
14	Research and Development	0	0	2	2	4	6
15	Overall satisfaction	0	1	2	1	3	7

The survey of those 14-point based on graduate attributes is mapped suitably to measure the attainment of POs and PSOs. The consolidated report of the parents survey in percentage is mapped against POs and PSOs presented in the following table

Employer's	PO	PS	PS	PS											
Survey	1	2	3	4	5	6	7	8	9	10	11	12	O1	O2	O3
Attainment	93	86	93	86	79	93	93	79	93	86	93	93	86	86	79

The Five point scale alumini survey is then into 3-point scale and mapped against POs and PSOs presented in the following

Employer's	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO .	PS O1	PS O2	PS O3
Survey		1000			1										
	2.7	2.5	2.7	2.5	2.3	2.7	2.7	2.3	2.7	2.5	2.7	2.7	2.5	2.5	2.3
Attainment	9	8	9	8	7	9	9	7	9	8	9	9	8	8	7
												P	2		



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	PO	PS	PS	PS											
Survey	1	2	3	4	5	6	7	8	9	10	11	12	01	O2	03
	2.5	2.3	2.5	2.3	2.7	2.3	2.7	2.5	2.7	2.5	2.7	2.5	2.3	2.7	2.5
Exit survey	8	7	8	7	9	7	9	8	9	8	9	8	7	9	8
Alumini	2.5	2.9	2.5	2.3	2.5	2.3	2.9	2.9	2.5	2.5	2.9	2.5	2.5	2.9	2.5
Survey	8	7	8	4	8	4	7	7	8	8	7	8	8	7	8
Parent's	2.5	2.3	2.7	2.5	2.5	2.3	2.7	2.5	2.7	2.5	2.5	2.7	2.5	2.7	2.7
Survey	8	7	9	8	8	7	9	8	9	8	8	9	8	9	9
Employer	2.7	2.5	2.7	2.5	2.3	2.7	2.7	2.3	2.7	2.5	2.7	2.7	2.5	2.5	2.3
Survey	9	8	9	8	7	9	9	7	9	8	9	9	8	8	7
	2.6	2.5	2.6	2.5	2.5	2.5	2.8	2.6	2.7	2.5	2.7	2.6	2.5	2.7	2.5
Average	3	7	9	2.3	8	2.3	3	3	4	8	8	9	3	4	8



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ALUMNI FEEDBACK ON PROGRAMME OUTCOME

FULL NAME	M. Suganua
GRADUATION YEAR	M. Suganya
CURRENT JOB TITLE	Electronic Engineering traning
COMPANY/ORGANIZATION	Athisakthi propert port limiter.
E-MAIL ID	Suganyamohan 261@gamil. com.

Thank you for taking the time to participate in our Alumni survey. Your feedback is valuable in helping us understand how well our institution prepares students, for the workplace. Please provide honest and thoughtful responses.

- Please rate the following skills and abilities based on your experience in the workplace.
- Use a scale from 1 to 5, where 1 is "Not at all proficient and 5 is "Extremely proficient".

S.NO	Assessmen	nt of Skills and Abilities	0	1	2	3	4	5
1		Verbal Communication				F		
2	Communication Skills	Written Communication					~	Ť
3	Skills	Presentation Skills				~	•	1399
4		Analytical Thinking						1
5	Problem Solving	Creativity		No. 12			/	
6		Decision Making				100 m		1
7	Nation I	Team Collaboration						
8	Leadership	Decision Making				~	~	
)	e 0.246	Motivation and Guidance						-
0	Technical Skill			Fig.			_	
1	Professional Develo	pment						~
12	Career Advancemen	t and Satisfaction		2,73		7	12 m	V
sugges alumna o	stions you may have re	comments, insights, or egarding your experience as an ET	2	9º	ood seen	UVAS PRII pe of En	MUINCIPA nainee	THI L ring 8
3	100 mm			RAAK	Colleg No.1, N	PRII le of Er luthup Sulth	. 5	-lava

No.1, Muthupillai Palayam Road, Sulthanpet Post. Puducherry - 605 110





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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ALUMNI FEEDBACK ON PROGRAMME OUTCOME

FULL NAME	I. Muthumanan
GRADUATION YEAR	2023
CURRENT JOB TITLE	Junian Developer
COMPANY/ORGANIZATION	STRAIVE
E-MAIL ID	mulhu 2002 ram @gmail. com

Thank you for taking the time to participate in our Alumni survey. Your feedback is valuable in helping us understand how well our institution prepares students, for the workplace, Please provide honest and thoughtful responses.

- Please rate the following skills and abilities based on your experience in the workplace.
- Use a scale from 1 to 5, where 1 is "Not at all proficient and 5 is "Extremely proficient".

Assessmen	nt of Skills and Abilities	0	1	2	3	4	5
	Verbal Communication						n
Communication	Written Communication	Manufacture of				.0	1
SKIIIS	Presentation Skills				10		
D. 11 . C. 1.	Analytical Thinking						1
Problem Solving	Creativity					1	
	Decision Making	1					1
	Team Collaboration					1	
Leadership	Decision Making				1		
	Motivation and Guidance				2200		1
Technical Skill						1	
Professional Develop	oment					71	1
Career Advancement	t and Satisfaction						1
	Communication Skills Problem Solving Leadership Technical Skill Professional Develop	Communication Skills Written Communication Presentation Skills Analytical Thinking Creativity Decision Making Team Collaboration Decision Making Motivation and Guidance	Communication Skills Written Communication Presentation Skills Analytical Thinking Creativity Decision Making Team Collaboration Decision Making Motivation and Guidance Technical Skill Professional Development	Communication Skills Written Communication Presentation Skills Analytical Thinking Creativity Decision Making Team Collaboration Decision Making Motivation and Guidance Technical Skill Professional Development	Communication Skills Written Communication Presentation Skills Analytical Thinking Creativity Decision Making Team Collaboration Decision Making Motivation and Guidance Technical Skill Professional Development	Communication Skills Written Communication Presentation Skills Analytical Thinking Creativity Decision Making Team Collaboration Decision Making Motivation and Guidance Technical Skill Professional Development	Communication Skills Presentation Skills Analytical Thinking Creativity Decision Making Team Collaboration Decision Making Motivation and Guidance Professional Development

Please share any additional comments, insights, or suggestions you may have regarding your experience as an alumna/alumnus of RAAKCET

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Dr. S. SEENUVASAMURITAIN,

PRINCIPAL

RAAK College of Engineering & Technology

RAAK College of Engineering & Road,

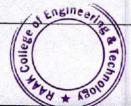
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Students Exit Survey

Name	D. Sooriya	
Batch	2019-2023	

Tick the box that best describes your level of satisfaction at each statement:

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
1	Basic Knowledge in Mathematics, Science, engineering and humanities.						1
2	Ability to identify, design, analyze and solve electronics engineering					1	9
3	Design/develop of complex engineering problems and its solution	20.00					1
4	Use of research-based knowledge and research methods					1	
5	Demonstrate the ability of advanced technologies to solve real time problems				1		
6	Awareness to apply engineering solutions in global, national and social contexts.						1
7	Understanding of Professional engineering solutions in societal and environmental contexts.					1	
8	Understand of professional and ethical responsibilities						1
9	Ability to function as an effective member in multi-disciplinary teams.					1	
10	Proficiency in English language in both communication and technical forms.				1		
11	Demonstrate the ability to choose and apply appropriate resources		F 11			1	
12	Capable of self-education and clear understanding of the value of updating their professional knowledge to engage inn life long learning						1
13	Exhibiting attitude of continues learning and deliver efficient solution for emerging challenges in the computation domain				1		
14	Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry		86.1.5 2011				1
15	Research and Development		0~	(i e	1







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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Students Exit Survey

Name	S. Crantham	
Batch	2019-2028	

Tick the box that best describes your level of satisfaction at each statement:

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
1	Basic Knowledge in Mathematics, Science, engineering and humanities.		100		A LEGIS		V
2	Ability to identify, design, analyze and solve electronics engineering					V	-
3	Design/develop of complex engineering problems and its solution				V		
4	Use of research-based knowledge and research methods	-			V		V
5	Demonstrate the ability of advanced technologies to solve real time problems					V	
6	Awareness to apply engineering solutions in global, national and social contexts.						V
7	Understanding of Professional engineering solutions in societal and environmental contexts.					r	
8	Understand of professional and ethical responsibilities			Mary I			
9	Ability to function as an effective member in multi-disciplinary teams.				V		V
10	Proficiency in English language in both communication and technical forms.					V	V
11	Demonstrate the ability to choose and apply appropriate resources		La constant				V
	Capable of self-education and clear understanding of the value of						V
12	updating their professional knowledge to engage inn life long learning					V	4.0
13	Exhibiting attitude of continues learning and deliver efficient solution for emerging challenges in the computation domain						V
14	Gaining critical understanding of hardware and software tools catering to the contemporary needs of IT industry					V	
15	Research and Development		D	00			/







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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Students Exit Survey

Name	N. Salhish	
Batch	2019 - 2023	

Tick the box that best describes your level of satisfaction at each statement:

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	14	1 5
1	Basic Knowledge in Mathematics, Science, engineering and humanities.				-	7	
2	Ability to identify, design, analyze and solve electronics engineering			100 mm			1
3	Design/develop of complex engineering problems and its solution					1	
4	Use of research-based knowledge and research methods				1		
5	Demonstrate the ability of advanced technologies to solve real time problems					1	1
6	Awareness to apply engineering solutions in global, national and social contexts.					1	/
7	Understanding of Professional engineering solutions in societal and environmental contexts.					1	
8	Understand of professional and ethical responsibilities						
9	Ability to function as an effective member in multi-disciplinary teams.		8				1
10	Proficiency in English language in both communication and technical forms.				1		1
11	Demonstrate the ability to choose and apply appropriate resources						
	Capable of self-education and clear understanding of the value of updating their professional knowledge to engage inn life long learning					1	
3	Exhibiting attitude of continues learning and deliver efficient solution for emerging challenges in the computation domain						
4	Gaining critical understanding of hardware and software tools catering to he contemporary needs of IT industry				1	V	
-	Research and Development		-		V 1		







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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING EMPLOYER'S FEEDBACK ON PROGRAMME OUTCOME

COMPANY NAME	WHIRPOOL
YOUR POSITION	
CONTACT NUMBER	quality controller 9790036350
E-MAIL ID	rarayananbasam@ gmail - com

Thank you for taking the time to participate in our Employer's survey. Your feedback is valuable in helping us understand how well our institution prepares students, for the workplace. Please provide honest and thoughtful responses.

- Please rate the following skills and abilities based on your experience in the workplace.
- Use a scale from 1 to 5, where 1 is "Not at all proficient and 5 is "Extremely proficient".

S.NO	Assessment of Abilities, Skills and Attributes acquired	0	1	2	3	4	5
1	Knowledge of Core Engineering Concepts	-	+				
2	Application of Technical Knowledge		1000	-		/	
3	Problem-Solving Abilities		1	1			1
	Communication Skills			2.25		~	
4	Communication (Verbal and Written)	-		1			
5	Teamwork and Collaboration		14	-	1		
6	Adaptability		2000	-		~	
	Practical Experience						V
7	Relevance of Academic Knowledge to the Workplace						
8	Ability to Apply Theory to Practical Situations				7	~	
9	Overall Work Performance						
	Specific Skills Asses						V
0	Project Management	nt					
1	Technical Proficiency			107	1779	1	
2	Professionalism					1539	1
3	Program Contribution to Workplace Success			100	~		
4	Research and Development				- 0.5		1
5 (Overall satisfaction					1	



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING EMPLOYER'S FEEDBACK ON PROGRAMME OUTCOME

COMPANY NAME	STRAIVE
YOUR POSITION	
CONTACT NUMBER	Junioy Druetopey
E-MAIL ID	20ya. Cogux207 & g mail. com

Thank you for taking the time to participate in our Employer's survey. Your feedback is valuable in helping us understand how well our institution prepares students, for the workplace. Please provide honest and thoughtful responses.

Please rate the following skills and abilities based on your experience in the workplace.

Use a scale from 1 to 5, where 1 is "Not at all proficient and 5 is "Extremely proficient".

S.NO	Assessment of Abilities, Skills and Attributes acquired	0	1	2	3	4	5
1	Knowledge of Core Engineering Concepts	-	+-	-	-		-
2	Application of Technical Knowledge	-	+	-	-	~	
3	Problem-Solving Abilities		-		-		1
	Communication Skills					1~/	
4	Communication (Verbal and Written)		T	1	1	1	-
5	Teamwork and Collaboration		-	+	1	-	
6	Adaptability	+	-	-	-	~	-
	Practical Experience			1			~
7	Relevance of Academic Knowledge to the Workplace					,	
8	Ability to Apply Theory to Practical Situations					V	1
9	Overall Work Performance				,		•
	Specific Skills Assessmen	\ <u>\</u>			V		
10	Project Management	1				- , 1	
11	Technical Proficiency	-				V	
12	Professionalism						/
3	Program Contribution to Workplace Success	and the same		20100		~	
4	Research and Development					-	V
5	Overall satisfaction			- 0		V	
		1		1	0/	\	V



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

EMPLOYER'S FEEDBACK ON PROGRAMME OUTCOME

COMPANY NAME	STRAIVE
YOUR POSITION	
CONTACT NUMBER	Jurier duelopen
E-MAIL ID	7358752797
	Vireshuerbat 5002 @ garail. com

Thank you for taking the time to participate in our Employer's survey. Your feedback is valuable in helping us understand how well our institution prepares students, for the workplace. Please provide honest and thoughtful responses.

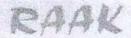
- Please rate the following skills and abilities based on your experience in the workplace.
- Use a scale from 1 to 5, where 1 is "Not at all proficient and 5 is "Extremely proficient".

S.NO	Assessment of Abilities, Skills and Attributes acquired	0	1	2	3	4	5
1	Knowledge of Core Engineering Concepts		-	-			-
2	Application of Technical Knowledge	+				V	
3	Problem-Solving Abilities	- C C C C C C C C C C	-		1000		V
	Communication Skills	les e				V	
4	Communication (Verbal and Written)		T	T-			_
5	Teamwork and Collaboration				+ •	V	
6	Adaptability	-		-			V
Y TOUR T	Practical Experience		I LES		V		
7	Relevance of Academic Knowledge to the Workplace				Τ,		
8	Ability to Apply Theory to Practical Situations				1	V	
9	Overall Work Performance	+	-			V	
	Specific Skills Assessmen	nt nt					V
0	Project Management	1					
1	Technical Proficiency					V	
2	Professionalism						V
	Program Contribution to Workplace Success					V	
4	Research and Development	-			V		
5	Overall satisfaction	-			18	K	



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PARENT'S FEEDBACK ON PROGRAMME OUTCOME

Student's Name	Kanthanuann T	
Parent's Name	Tohanadhan Go	
Relationship to student	FATHER.	

Thank you for taking the time to participate in our parent's survey. Your feedback is valuable in helping us understand how well our institution prepares for the workplace. Please provide honest and thoughtful responses.

- Please rate the following skills and abilities based on your experience in the workplace.
- Use a scale from 1 to 5, where 1 is "not at all proficient" and 5 is "Extremely Proficient"

Programme Satisfaction

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
1	Overall Satisfaction with the Program						/
2	Quality of Academic Instruction					~	
3	Relevance of Curriculum to Industry Needs						V
4	Availability of Resources(labs, materials, etc,)					1	
5	Communication from institution to Parents				/		

Specific Program Aspects

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
6	Availability and knowledge of subject matter		Lagran				/
7	Facility: Adequacy of Laboratories, Access to Technology, Study Spaces					~	
8	Student Support : Counseling services, Career guidance, Academic advising						~
9	Industry Exposure :Internship opportunities, Industry guest lectures and networking events				~		
10	Preparation for Future						V
11	Aptitude Training and placement					V	
12	Communication						V
13	Recommendation					V	
14	Research and Development						~
15	Overall						/
Pleas	Share any additional comments, suggestions or the share any have regarding RAAKCET.		6	joet	m	, ME P	n C.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PARENT'S FEEDBACK ON PROGRAMME OUTCOME

Student's Name	S - Rajolakehmi
Parent's Name	T. Sundharojah
Relationship to student	Ectron

Thank you for taking the time to participate in our parent's survey. Your feedback is valuable in helping us understand how well our institution prepares for the workplace. Please provide honest and thoughtful responses.

- > Please rate the following skills and abilities based on your experience in the workplace.
- > Use a scale from 1 to 5, where 1 is "not at all proficient" and 5 is "Extremely Proficient"

Programme Satisfaction

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
1	Overall Satisfaction with the Program						V
2	Quality of Academic Instruction					V	
3	Relevance of Curriculum to Industry Needs						V
4	Availability of Resources(labs, materials, etc.)					V	
5	Communication from institution to Parents				V		

Specific Program Aspects

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
6	Availability and knowledge of subject matter						V
7	Facility: Adequacy of Laboratories, Access to Technology, Study Spaces					V	
8	Student Support : Counseling services, Career guidance, Academic advising					,	V
9	Industry Exposure :Internship opportunities, Industry guest lectures and networking events					~	
10	Preparation for Future				V		
11	Aptitude Training and placement						V
12	Communication					V	Sin
13	Recommendation						V
14	Research and Development						
15	Overall				M.	2	
Please	Shairean additional comments, suggestions or		-	nnol	SM	77	

wave regarding RAAKCET.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PARENT'S FEEDBACK ON PROGRAMME OUTCOME

Student's Name	S. Sirami	
Parent's Name	S. Saravanan	
Relationship to student	tather	

Thank you for taking the time to participate in our parent's survey. Your feedback is valuable in helping us understand how well our institution prepares for the workplace. Please provide honest and thoughtful responses.

- > Please rate the following skills and abilities based on your experience in the workplace.
- > Use a scale from 1 to 5, where 1 is "not at all proficient" and 5 is "Extremely Proficient"

Programme Satisfaction

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
1	Overall Satisfaction with the Program						
2	Quality of Academic Instruction						
3	Relevance of Curriculum to Industry Needs					-	
4	Availability of Resources(labs, materials, etc.)						
5	Communication from institution to Parents			i e			4-9

Specific Program Aspects

S.No	Assessment of abilities, skills and attributes acquired	0	1	2	3	4	5
6	Availability and knowledge of subject matter		5.5	1			5
7	Facility: Adequacy of Laboratories, Access to Technology, Study Spaces					7	
8	Student Support : Counseling services, Career guidance, Academic advising						1
9	Industry Exposure :Internship opportunities, Industry guest lectures and networking events				1		
10	Preparation for Future					5	
11	Aptitude Training and placement						1
12	Communication						
13	Recommendation					7	
14	Research and Development						
15	Overall				10.		_
Please	statements additional comments, suggestions or ou making regarding RAAKCET.		Gjo	rad T	MAN	1 P11, id	E. Ph.C.

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